

The **LTC<sup>®</sup>1067** Initial Release data sheet has been modified. The changes/additions are shown in **bold** type. The main change is the introduction of the LTC1067 in the **GN Package (16-Pin Narrow SSOP)**. For all other specifications, typical performance curves and applications information, please see the **LTC1067** data sheet.

## PACKAGE/ORDER INFORMATION

TOP VIEW		ORDER PART NUMBER
V <sup>+</sup> 1	16 CLK	<b>LTC1067CGN</b> <b>LTC1067-50CGN</b> <b>LTC1067IGN</b> <b>LTC1067-50IGN</b>
NC 2	15 AGND	
V <sup>+</sup> 3	14 V <sup>-</sup>	
SA 4	13 SB	
LPA 5	12 LPB	
BPA 6	11 BPB	
HPA/NA 7	10 HPB/NB	
INV A 8	9 INV B	
<b>GN PACKAGE</b> <b>16-LEAD NARROW PLASTIC SSOP</b> <b>T<sub>JMAX</sub> = 110°C, θ<sub>JA</sub> = 135°C/W</b>		

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## ELECTRICAL CHARACTERISTICS

**LTC1067 (complete filter) V<sub>S</sub> = 4.75V, f<sub>CLK</sub> = 250kHz, T<sub>A</sub> = 25°C, unless otherwise noted.**

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Clock-to-Center Frequency, f <sub>CLK</sub> /f <sub>0</sub>	V <sub>S</sub> = 3V, f <sub>CLK</sub> = 250kHz, Mode 1, f <sub>0</sub> = 2.5kHz, Q = 5 R1 = R3 = 49.9k, R2 = 10k	●	100:1 ±0.2	±0.70	%
	V <sub>S</sub> = 4.75V, f <sub>CLK</sub> = 250kHz, Mode 1, f <sub>0</sub> = 2.5kHz, Q = 5 R1 = R3 = 49.9k, R2 = 10k	●	100:1 ±0.2	±0.70	%
	V <sub>S</sub> = ±5V, f <sub>CLK</sub> = 500kHz, Mode 1, f <sub>0</sub> = 5kHz, Q = 5 R1 = R3 = 49.9k, R2 = 10k	●	100:1 ±0.2	±0.70	%
Clock-to-Center Frequency Ratio, Side-to-Side Matching	V <sub>S</sub> = 3V, f <sub>CLK</sub> = 250kHz, Q = 5	●	±0.1	±0.35	%
	V <sub>S</sub> = 4.75V, f <sub>CLK</sub> = 250kHz, Q = 5	●	±0.1	±0.35	%
	V <sub>S</sub> = 5V, f <sub>CLK</sub> = 500kHz, Q = 5	●	±0.1	±0.35	%
Power Supply Current	V <sub>S</sub> = 3V, f <sub>CLK</sub> = 250kHz	●	2.50	4.5	mA
	V <sub>S</sub> = 4.75V, f <sub>CLK</sub> = 250kHz	●	4.35	5.5	mA
	V <sub>S</sub> = 5V, f <sub>CLK</sub> = 500kHz	●	4.35	7.5	mA

**LTC1067-50 (complete filter) V<sub>S</sub> = 4.75V, f<sub>CLK</sub> = 125kHz, T<sub>A</sub> = 25°C, unless otherwise noted.**

Clock-to-Center Frequency, f <sub>CLK</sub> /f <sub>0</sub>	V <sub>S</sub> = 3V, f <sub>CLK</sub> = 125kHz, Mode 1, f <sub>0</sub> = 5kHz, Q = 5 R1 = R3 = 49.9k, R2 = 10k	●	50:1 ±0.2	±0.75	%
	V <sub>S</sub> = 4.75V, f <sub>CLK</sub> = 125kHz, Mode 1, f <sub>0</sub> = 5kHz, Q = 5 R1 = R3 = 49.9k, R2 = 10k	●	50:1 ±0.2	±0.75	%
Clock-to-Center Frequency Ratio, Side-to-Side Matching	V <sub>S</sub> = 3V, f <sub>CLK</sub> = 125kHz, Q = 5	●	±0.2	±0.55	%
	V <sub>S</sub> = 4.75V, f <sub>CLK</sub> = 125kHz, Q = 5	●	±0.2	±0.55	%
	V <sub>S</sub> = 5V, f <sub>CLK</sub> = 250kHz, Q = 5	●	±0.2	±0.55	%
Power Supply Current	V <sub>S</sub> = 3V, f <sub>CLK</sub> = 125kHz	●	1.00	2.5	mA
	V <sub>S</sub> = 4.75V, f <sub>CLK</sub> = 125kHz	●	1.45	3.0	mA
	V <sub>S</sub> = 5V, f <sub>CLK</sub> = 250kHz	●	2.35	4.0	mA

For further information regarding this specification notice contact:

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